2. component
3. solder
4. componentmask
5. soldermask
6. plated-drill
7. topsilk
8. bottomsilk
9. toppaste
10. topassembly
11. bottomassembly
12. fab

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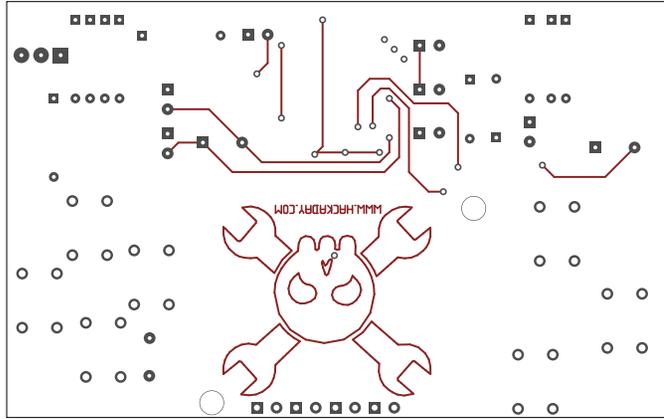
Layer component

Layer solder
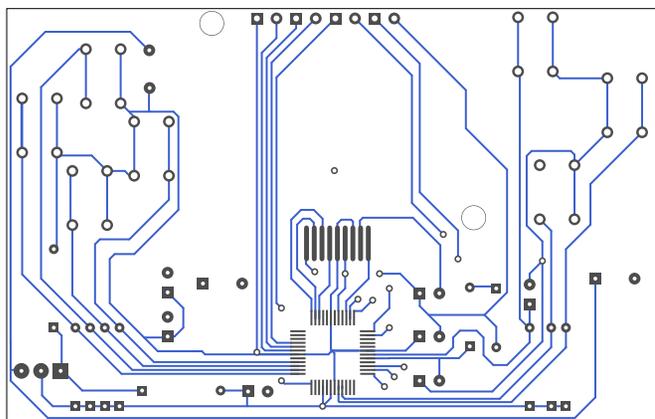
Layer componentmask

Layer Soldermask
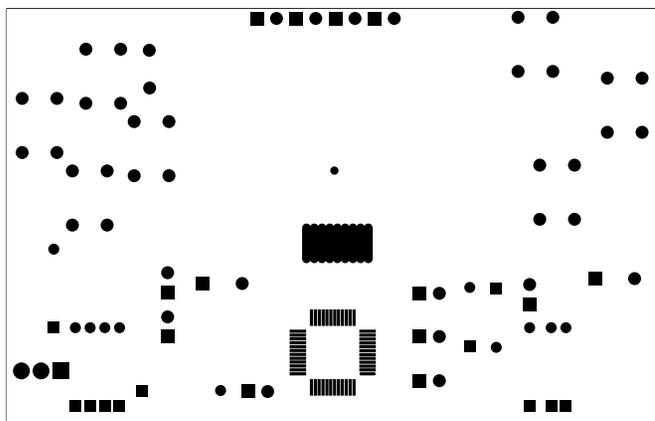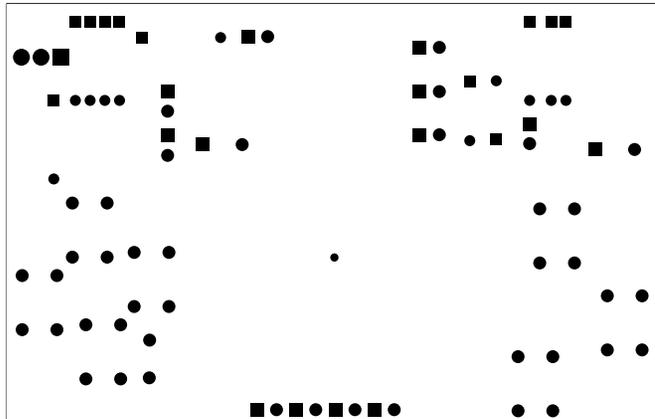
Layer plated-drill

Layer topsilk

+5..12V

debug conn

gnd
rxd1
txd1
p0.7
ad0.5
txd0
rxd0
3V

Optional:
reset

gnd

A

'D-pad'

nokia 5110
LCD

B

30pf*2

1n4001*2

100nf

10uF

1K8

12.288MHz

+

LM317L

22k*3

22k*4

1K

100nf

lpc2103

100nF*3

Layer bottomsilk
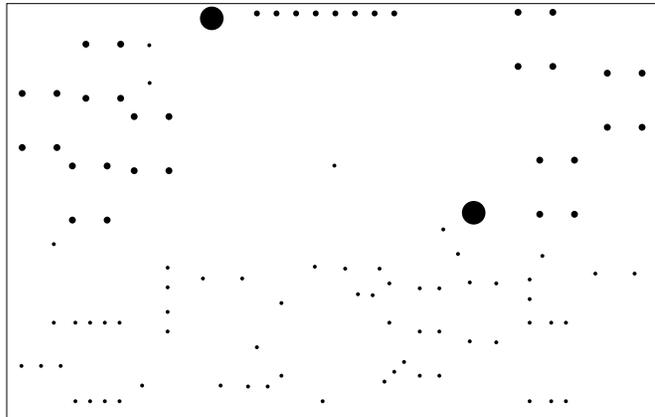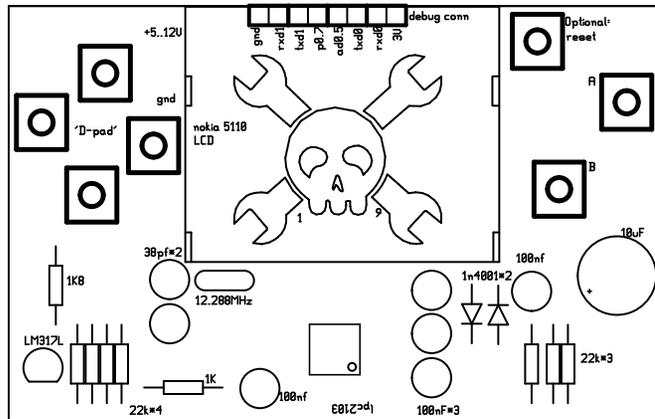
Layer toppaste
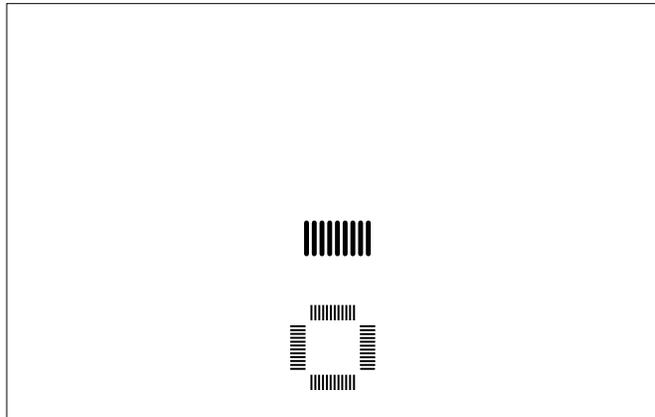
Layer topassembly

Layer bottomassembly
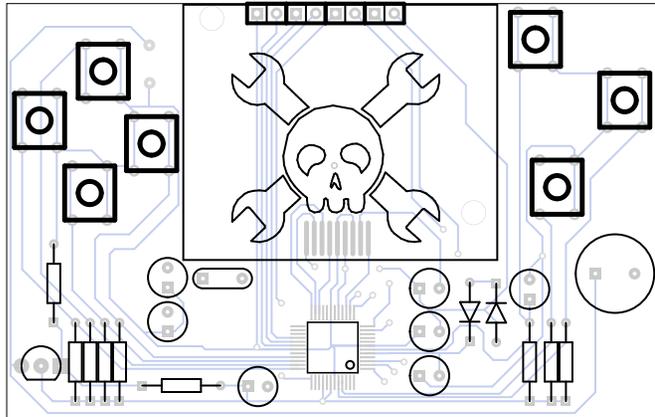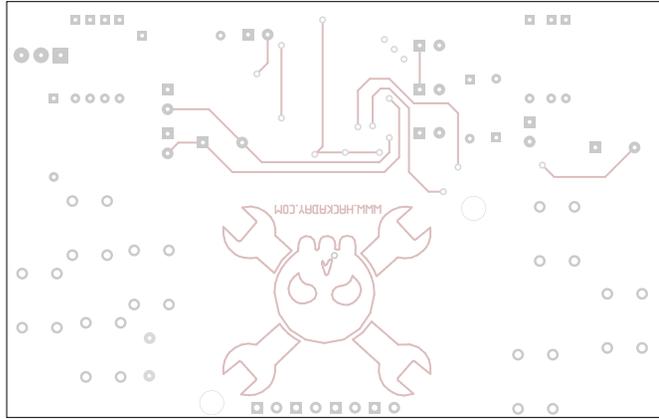
There are 4 different drill sizes used in this layout, 101 holes total

Symbol	Diam. (Inch)	Count	Plated?	
⊤	0.020	63	YES	Title: chip8emu - Fabrication Drawing
+	0.030	8	YES	Author: Jeroen Domburg
×	0.035	28	YES	Date: Sun Nov 26 00:39:23 2006 UTC
∘	0.120	2	YES	Maximum Dimensions: 3358 mils wide, 2118 mils high
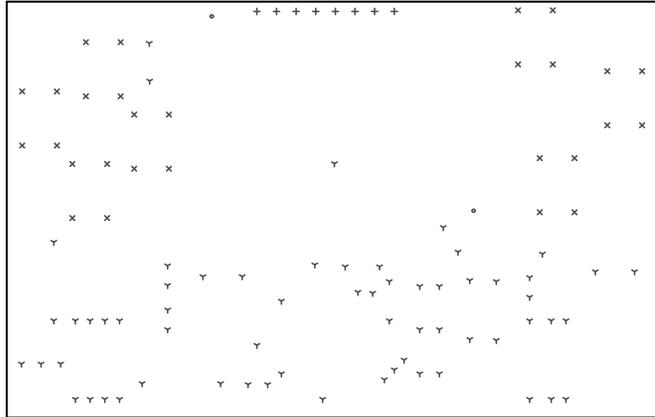
Board outline is the centerline of this 10 mil rectangle - 0,0 to 3358,2118 mils